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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application No.	09/888,296
		Filing Date	June 22, 2001
		First Named Inventor	David A. Fotland
		Art Unit	2183
		Examiner Name	Charles A. Harkness
		Attorney Docket Number	20880-06029
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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document No. Number - Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
CH	A1	US-6,085,215	07-04-2000	Ramakrishnan et al.
		US-		

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T ⁶

OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
CH	C1	EGGERS, S. et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors," IEEE Micro, October 1997, pp. 12-19	

Examiner Signature	<i>Charles A. Harkness</i>	Date Considered	1/19/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

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